

#4

XA-9594

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

MATSUOKA, Hideyuki et al.

Intl. Appln. No.: PCT/JP00/03723

Intl. Filing Date: 8 June 2000

U.S. Appln. No.: 10/009,826

For: SEMICONDUCTOR MEMORY DEVICE AND MANUFACTURING METHOD
THEREOF

* * *

03/22/2002 MAL111 00000046 10009826 REPLY TO NOTIFICATION OF MISSING REQUIREMENTS
01 FC:154 130.00 DP UNDER 35 U.S.C. § 371

Box PCT
Assistant Commissioner for Patents
Washington, D.C. 20231

Attention: DO/EO/US

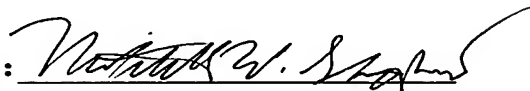
Sir:

Replying to the Notification (copy enclosed) mailed
February 26, 2002, Applicants submit herewith a Declaration
and a check for \$170.00 covering the required surcharge
under 37 C.F.R. § 1.492(e) (\$130) and the recording fee
(\$40) for recordation of the accompanying Assignment.

The Commissioner is hereby authorized to charge to
Deposit Account No. 50-1165 any fees that may be required

by this paper and to credit any overpayment to that Account. If any extension of time is required in connection with the filing of this paper and has not been requested separately, such extension is hereby requested.

Respectfully submitted,

By: 
Mitchell W. Shapiro
Reg. No. 31,568

MWS:sjk

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March 19, 2002